

# Integrated Low Profile Transceiver Module for Telecom Applications - IrDA Standard

#### **Description**

The miniaturized TFDU4202 is an ideal transceiver for applications in telecommunications like mobile phones and pagers. The device is mechanically designed for lowest profile with a height of only 2.8 mm. The infrared transceiver is compatible to the latest IrDA® IrPHY specification up to a data rate of 115 kbit/s. At lower operating voltages up to 3.3 V the transceiver can be operated without external current limiting resistor to achieve a range > 1 m.

The added feature is a split power supply for IRED driver ( $V_{CCD}$ ) and ASIC ( $V_{CCD}$ ).

For operating only in the limited distance, low power range (20 cm/ 30 cm), TFDU4201 with built-in current control is recommended. For this device see the appropriate data sheet.





#### **Features**

- Package dimension microFace TFDU4202:
  - L 7.1 mm x W 4.7 mm x H 2.8 mm
- Compatible to latest IrDA IrPHY standard
- CIR Remote Control operation:
   Typical transmission range 8 m using standard RC-receivers. Receives RC-commands with typical specified sensitivity.
- SMD Side View
- Lowest power consumption 65 μA, receive mode, 0.01 μA Shutdown
- Built-in current limitation
- · Output intensity adjustable by external resistor
- Wide supply voltage range (2.4 V to 5.5 V)
- Split power supply

- Operational down to 2.0 V
- Fewest external components
- Eye safety: Double safety
   Measures:
   Limited optical output oulse duration
   Limited optical output intensity
  - Limited optical output intensity IEC60825-1, 2001: Class 1
- Push-pull output (RXD)
- High EMI immunity
- Split power supply, transmitter and receiver can be operated from two power supplies with relaxed requirements saving costs, US Patent No. 6,157,476
- · Lead (Pb)-free device
- Device in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **Applications**

- · Mobile Phones
- Pagers
- Personal Digital Assistants (PDA)
- Handheld Battery Operated Equipment

#### **Parts Table**

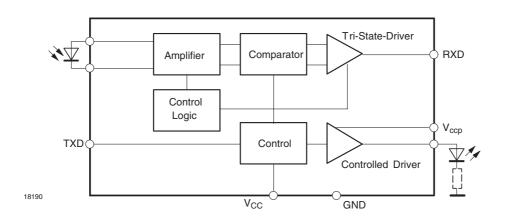
Part	Description	Qty / Reel	
TFDU4202-TR1	Orientated in carrier tape for side view in mounting	750 pcs	
TFDU4202-TR3	Orientated in carrier tape for side view in mounting	2250 pcs	

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Rev. 1.8, 05-Dec-05



# **Functional Block Diagram**



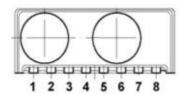
### **Pin Description**

Pin Number	Function	Description	I/O	Active
1	IRED GND	IRED cathode, ground		
2	IRED GND	IRED cathode, ground		
3	RXD	Output, received data, push-pull output	0	low
4	V <sub>CCP</sub>	IRED supply voltage		
5	GND	Ground		
6	GND	Ground		
7	TXD	Input, transmit data	I	high
8	V <sub>CC</sub> *)	Power supply voltage	I	high

 $<sup>^{*)}</sup>$  V<sub>CC</sub> is allowed to be switched off for standby with V<sub>ccp</sub> applied.

#### **Pinout**

TFDU4202 weight 100 mg



18228



### **Absolute Maximum Ratings**

Reference Point Pin 8, unless otherwise noted.

Parameter	Test Conditions	Symbol	Min	Тур.	Max	Unit
Supply voltage range		$V_{CC}$	- 0.5		6	V
		V <sub>ccp</sub>	- 0.5		6	V
Input current	all pins				10	mA
Output sink current					25	mA
Power dissipation		$P_{tot}$			200	mW
Junction temperature		$T_J$			125	°C
Ambient temperature range (operating)		T <sub>amb</sub>	- 25		85	°C
Storage temperature range		T <sub>stg</sub>	- 40		100	°C
Soldering temperature	see the chapter "Soldering conditions" for lead-bearing and Pb-free processing				260	°C
Average IRED current		I <sub>IRED(DC)</sub>			125	mA
Repetitive pulsed IRED current	< 90μs, t <sub>on</sub> < 20 %	I <sub>IRED(RP)</sub>			500	mA
Transmitter data input voltage		$V_{TXD}$	- 0.5		6	V
Receiver data output voltage		$V_{RXD}$	- 0.5		6	V

# **Eye safety information**

Parameter	Test Conditions	Symbol	Min	Тур.	Max	Unit
Virtual source size	Method: (1 - 1/e) encircled	d		2		mm
	energy					

Compatible to Class 1 operation of IEC 60825 or EN60825 with worst case IrDA SIR pulse pattern, 115.2 kbit/s

#### **Electrical Characteristics**

#### **Transceiver**

Tested for the following parameters ( $V_{CC}$  = 2.4 V to 5.5 V, - 25 °C to + 85 °C, unless otherwise stated).

Parameter	Test Conditions	Symbol	Min	Тур.	Max	Unit
Supported data rates	base band		9.6		115.2	kbit/s
Supply voltage range	operational down to 2.0 V	V <sub>CC</sub>	2.4		5.5	V
Supply current	V <sub>CC</sub> = 2.4 V to 5.5 V, E <sub>e</sub> = 0, receive mode, full temperature range	I <sub>S</sub>		65	100	μΑ
	V <sub>CC</sub> = 2.4 V to 5.5 V, 10 klx sunlight, receive mode, full temperature range, no signal	I <sub>S</sub>		70	100	μΑ
	V <sub>CC</sub> = 2.7 V, V <sub>ccp</sub> = 2.7 V, 115.2 kbit/s transmission, receive mode, nose to nose operation	I <sub>S</sub>		1		mA
Supply current at V <sub>ccp</sub>	shutdown mode, entire temperature range 20 °C	I <sub>Sshdown</sub>		0.02	1	μΑ
	V <sub>CC</sub> = 0 V, entire temperature range 20 °C	I <sub>Sshdown</sub>			10	nA
IRED peak current transmitting	$I_e$ = 40 mW/sr, no external resistor $V_{ccp}$ = 2.7 V, SIR standard	I <sub>Str</sub>			360	mA
Transceiver 'power on' settling time	time from switching on V <sub>CC</sub> to established specified operation				1	ms

Document Number 82541 www.vishay.com

Rev. 1.8, 05-Dec-05

# **TFDU4202**

# **Vishay Semiconductors**



# **Optoelectronic Characteristics**

#### Receiver

Tested for the following parameters ( $V_{CC}$  = 2.4 V to 5.5 V, - 25 °C to + 85 °C, unless otherwise stated).

Parameter	Test Conditions	Symbol	Min	Тур.	Max	Unit
Minimum detection threshold irradiance (logic high receiver input irradiance)	$ \alpha  \le \pm 15$ °, V <sub>CC</sub> = 2.4 V to 5.5 V	E <sub>e, min</sub>		25 (2.5)	50 (5)	mW/m <sup>2</sup> (μW/cm <sup>2</sup> )
	2.0 V, 25 °C tested	E <sub>e, min</sub>		50	100	mW/m <sup>2</sup>
Maximum detection threshold irradiance	$\mid \alpha \mid \leq \pm 90$ °, $V_{CC} = 5$ V	E <sub>e, max</sub>	3300 (330)	5000 (500)		W/m <sup>2</sup> (mW/cm <sup>2</sup> )
	$\mid \alpha \mid \leq \pm 90$ °, $V_{CC} = 3$ V	E <sub>e, max</sub>	8000 (800)	15000 (1500)		W/m <sup>2</sup> (mW/cm <sup>2</sup> )
Logic low receiver input irradiance		E <sub>e, max,low</sub>			4 (0.4)	mW/m <sup>2</sup> (μW/cm <sup>2</sup> )
		E <sub>e, max,low</sub>				μW/m <sup>2</sup>
Output voltage RXD	active, C = 15 pF	V <sub>OL</sub>	0		0.5	V
	non active, C = 15 pF	V <sub>OH</sub>	V <sub>CC</sub> - 0.5			V
Output current RXD	V <sub>OL</sub> < 0.5 V				4	mA
Rise time at load	C = 15 pF, R = 2.2 k $\Omega$	t <sub>r</sub>	20		70	ns
Fall time at load	C = 15 pF, R = 2.2 k $\Omega$	t <sub>f</sub>	20		70	ns
RXD signal electrical output pulse width	2.4 kbit/s, input pulse width 1.41 µs to 3/16 of bit duration	t <sub>p</sub>	1.41		20	μs
	115.2 kbit/s, input pulse width 1.41 µs to 3/16 of bit duration	t <sub>p</sub>	1.41		4.5	μs
Output delay time (RXD), leading edge optical input to electrical output	output level = 0.5 x V <sub>CC</sub> @ 40 mW/m <sup>2</sup>	t <sub>dl</sub>		1	2	μs
Jitter, leading edge of output signal	over a period of 10 bit, 115.2 kbit/s	t <sub>j</sub>			400	ns
Output delay time (RXD), trailing edge optical input to electrical output	output level = 0.5 x V <sub>CC</sub> @ 40 mW/m <sup>2</sup>	t <sub>dt</sub>			6.5	μs
Power on time, SD recovery time				0.1	1	ms
Latency		t <sub>L</sub>		100	200	μs

www.vishay.com
Document Number 82541
Rev. 1.8, 05-Dec-05





#### **Transmitter**

Parameter	Test Conditions	Symbol	Min	Тур.	Max	Unit
Logic low transmitter input voltage		$V_{IL(TXD)}$	- 0.5		0.15 x V <sub>CC</sub>	V
Logic high transmitter input voltage		V <sub>IH(TXD)</sub>	0.8 x V <sub>CC</sub>		6	V
Output radiant intensity	$I_{F1}$ = 320 mA, $ \alpha  \le \pm$ 15 °, current controlled by external resistor, voltage range 2.7 V to 5.5 V	l <sub>e</sub>	45			mW/sr
Peak emission wavelength		$\lambda_{p}$	850		900	nm
Spectral emission bandwidth				60		nm
Optical rise/fall time	115.2 kHz square wave signal (duty cycle 1 : 1)				200	ns
Optical output pulse duration	input pulse duration 1.6 μs		1.5	1.6	1.7	μs
	input pulse duration > 25 μs, safety protection				25	μs
Output radiant intensity	logic low level				0.04	μW/sr
Overshoot, optical					25	%
Rising edge peak to peak jitter	over a period of 10 bits, independent of information content	t <sub>j</sub>			0.2	μѕ

# Truth table

		Inputs	Out	puts
V <sub>CC</sub>	TXD	Optical input Irradiance mW/m <sup>2</sup>	RXD	Transmitter
low	х	Х	low	0
high	high	x	high	l <sub>e</sub>
	high ≥ 25 μs	Х	high	0
	low	< 4	high	0
	low	<ul><li>Min. detection threshold irradiance</li><li>Max. detection threshold irradiance</li></ul>	х	0
	low	> Max. detection threshold irradiance	undefined	0



#### **Application Hints**

The TFDU4202 does not need any external component when operated with a "clean" power supply. In a more noisy ambient it is recommended to add a capacitor C1 and a resistor R1 for noise suppression. A combination of a tantalum with a ceramics capacitor will be efficient to attenuate both, RF and LF. The power supply V<sub>ccp</sub> must be able to source up to 550 mA current with a fast rise time. If that cannot be guaranteed an additional capacitor near pin 4 (V<sub>ccn</sub>) should be included. The value is depended on the power supply quality. A good choice between 4.7 μF and 10 µF.

#### Shut down

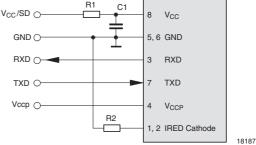
The TFDU4202 can be shut down (disabled) by setting the V<sub>PP</sub> pin 8 low. The RXD output is floating when the devices are in shut down mode. The TXD input is high impedance in shut down mode.

#### Latency

The receiver is in specified conditions after the defined latency. In a UART related application after that time (typically 50 µs) the receiver buffer of the UART must be cleared. Therefore the transceiver has to wait at least the specified latency after receiving the last bit before starting the transmission to be sure that the corresponding receiver is in a defined state. For more application circuits, see IrDC Design Guide

and TOIM4232 data sheet.

#### **Recommended Circuit Diagram**



## **Table Recommended Application Circuit Components**

Component	Recommended Value	Vishay Part Number
C1	4.7 μ <b>F</b> , 16 V	293D 475X9 016B 2T
R1	5 $\Omega$ ( 2 $\Omega$ to 47 $\Omega$ )	

This is a recommendation for a combination to start with to exclude power supply effects. Optimum, from a costs point of view, to work without both.

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#### **Temperature Derating Diagram**

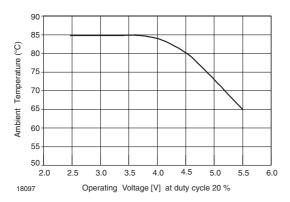


Figure 1. Temperature Derating Diagram

The temperature derating diagram shows the maximum operating temperature when the device is operated without external current limiting resistor. A power dissipating resistor of 2  $\Omega$  is recommended from the cathode of the IRED to Ground for supply voltages above 4 V. In that case the device can be operated up to 85  $^{\circ}\text{C}$ , too.

# Recommended Solder Profile Solder Profile for Sn/Pb soldering

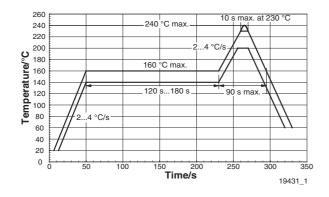


Figure 2. Recommended Solder Profile for Sn/Pb soldering

#### Lead-Free, Recommended Solder Profile

The TFDU4202 is a lead-free transceiver and qualified for lead-free processing. For lead-free solder paste like Sn-(3.0 - 4.0)Ag-(0.5 - 0.9)Cu, there are two standard reflow profiles: Ramp-Soak-Spike (RSS) and Ramp-To-Spike (RTS). The Ramp-Soak-Spike profile was developed primarily for reflow ovens heated by infrared radiation. With widespread use of forced convection reflow ovens the Ramp-To-Spike profile is used increasingly. Shown below in figure 2 is Vishay's recommended profile for use with the TFDU4202 transceivers. For more details please refer to Application note: SMD Assembly Instruction.

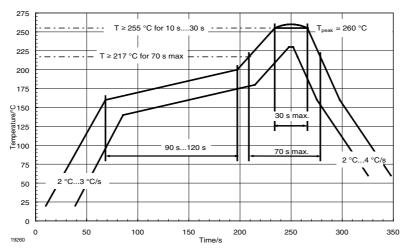


Figure 3. Solder Profile, RSS Recommendation



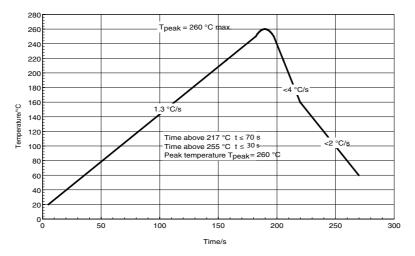


Figure 4. RTS Recommendation

A ramp-up rate less than 0.9 °C/s is not recommended. Ramp-up rates faster than 1.3 °C/s could damage an optical part because the thermal conductivity is less than compared to a standard IC.

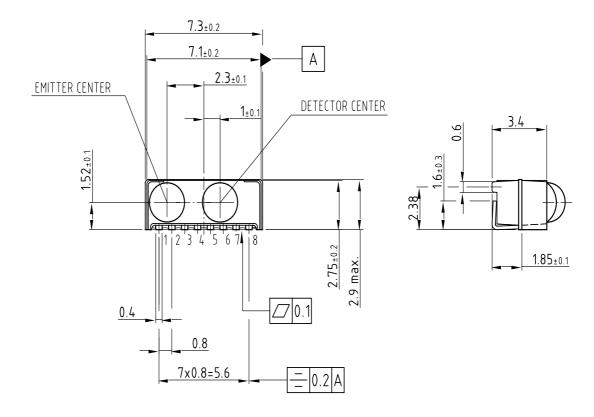
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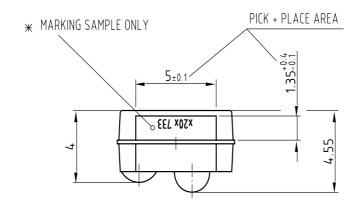
Document Number 82541

Rev. 1.8, 05-Dec-05



# **Package Dimensions**





All dimensions in mm



19821

\* MARKING ORIENTATION 180 DEGREES ALLOWED

Drawing-No.: 6.550-5185.01-4

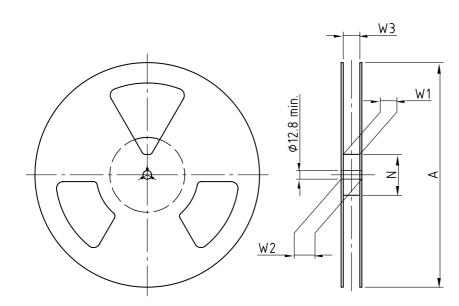
Issue: 5; 02.09.05

Figure 5. Package drawing, TFDU4202

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#### **Reel Dimensions**

14017



Ø20.2 min.

Reel hub 2:1

All dimensions in mm

technical drawings according to DIN specifications

Form of the leave open of the wheel is supplier specific.

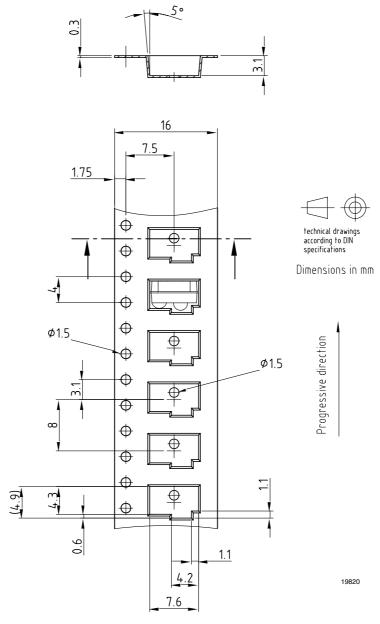
Dimension acc. to IEC EN 60 286-3

mm	mm	mm	mm	mm	mm	mm
Tape Width	A max.	N	W <sub>1</sub> min.	W <sub>2</sub> max.	W <sub>3</sub> min.	W <sub>3</sub> max.
16	180	60	16.4	22.4	15.9	19.4
16	330	50	16.4	22.4	15.9	19.4

www.vishay.com Document Number 82541 10 Rev. 1.8, 05-Dec-05



# **Tape Dimensions**



Drawing-No.: 9.700-5227.01-4

Issue: 3; 03.09.99

Figure 6. Tape drawing, TFDU4202 for side view mounting

# **TFDU4202**

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